

# DL004D IC – Hex Inverter (NOT Gate) DIP

## Datasheet

The DL004D is a TTL integrated circuit based on Low-Power Schottky (LS) technology. It contains six independent NOT gates (hex inverter) in a 14-pin DIP package. The device is equivalent to the SN74LS04 logic inverter family.

### Main Features

• TTL Low-Power Schottky technology.
• Six independent inverter gates.
• Standard 14-pin DIP package.
• Compatible with TTL logic levels.
• Fast switching performance.
• Low power consumption.
• Operating voltage: 5V.

### Operating Conditions

Parameter	Min	Typ	Max	Unit
Supply Voltage (VCC)	4.75	5	5.25	V
High-Level Output Current	-	-	400	μA
Low-Level Output Current	-	-	8	mA
Operating Temperature	0	-	+70	°C

### Electrical Characteristics

Parameter	Value
High-Level Input Voltage (VIH)	Min 2V
Low-Level Input Voltage (VIL)	Max 0.8V
Input Clamp Voltage	1.5V
High-Level Output Voltage (VOH)	Min 2.7V
Low-Level Output Voltage (VOL)	Max 0.5V
Propagation Delay (L → H)	15ns
Propagation Delay (H → L)	15ns

### Pin Configuration

Pin	Function	Pin	Function
1	A1	8	Y4
2	Y1	9	A4
3	A2	10	Y5
4	Y2	11	A5
5	A3	12	Y6

6	Y3	13	A6
7	GND	14	VCC

## Logic Function

Input A	Output Y
0	1
1	0