

Ordering Information

FZH 141: Q67000-H194
FZH 145: Q67000-H256

FZH 141
FZH 145

Dual NAND Power Gates, Each with Five Inputs and N Connection

The data are identical to FZH 131 and FZH 135, except for the values specified here.

Static Characteristics

12 V Operating Range

Temperature Ranges 1 and 5

Low-Level Output Voltage

U_{QL}

Test Conditions	Test Circuit	Lower Limit B	Typical	Upper Limit A	Unit
$U_S = U_{SB}$ $U_{IH} = 7.5\text{ V}$ $I_{QL} = 45\text{ mA}$	1	–	1.3	1.7	V

15 V Operating Range

Temperature Ranges 1 and 5

Low-Level Output Voltage

U_{QL}

$U_S = U_{SB}$ $U_{IH} = 7.5\text{ V}$ $I_{QL} = 54\text{ mA}$	1	–	1.4	1.7	V
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Logic Data (Per Gate)

Output Fan-Out (High Level)

F_{OH}

100

Output Fan-Out (Low Level)

F_{OL}

30

Input Fan-In per Input

F_I

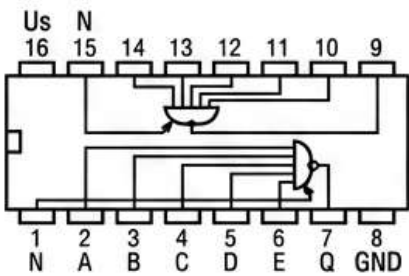
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Logic Function

$$Q = \overline{A \cdot B \cdot C \cdot D \cdot E}$$

Pin Assignment

Top View



Logic Diagram for one gate

